

Notice of References Cited	Application/Control No. 09/312,230	Applicant(s)/Patent Under Reexamination ATALLAH ET AL.	
	Examiner Matthew B Smithers	Art Unit 2134	Page 1 of 2

U.S. PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

★		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
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	V	Abadi et al, "Secure Circuit Evaluation", 1990, Journal of Cryptology, 3, pages 1-12.
	W	Matsumoto et al, "Speeding Up Secret Computations with Insecure Auxilary Devices", 1990, Springer-Verlag, pp. 497-506.
	X	Kawamura et al, "Fast Server-Aided Secret Computation Protocols for Modular Exponentiation", 1993, IEEE, pp. 778-784.

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	Examiner Matthew B Smithers	Art Unit 2134	Page 2 of 2

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